



Integrated ColdFire® Version 2 Microprocessor

MCF524X

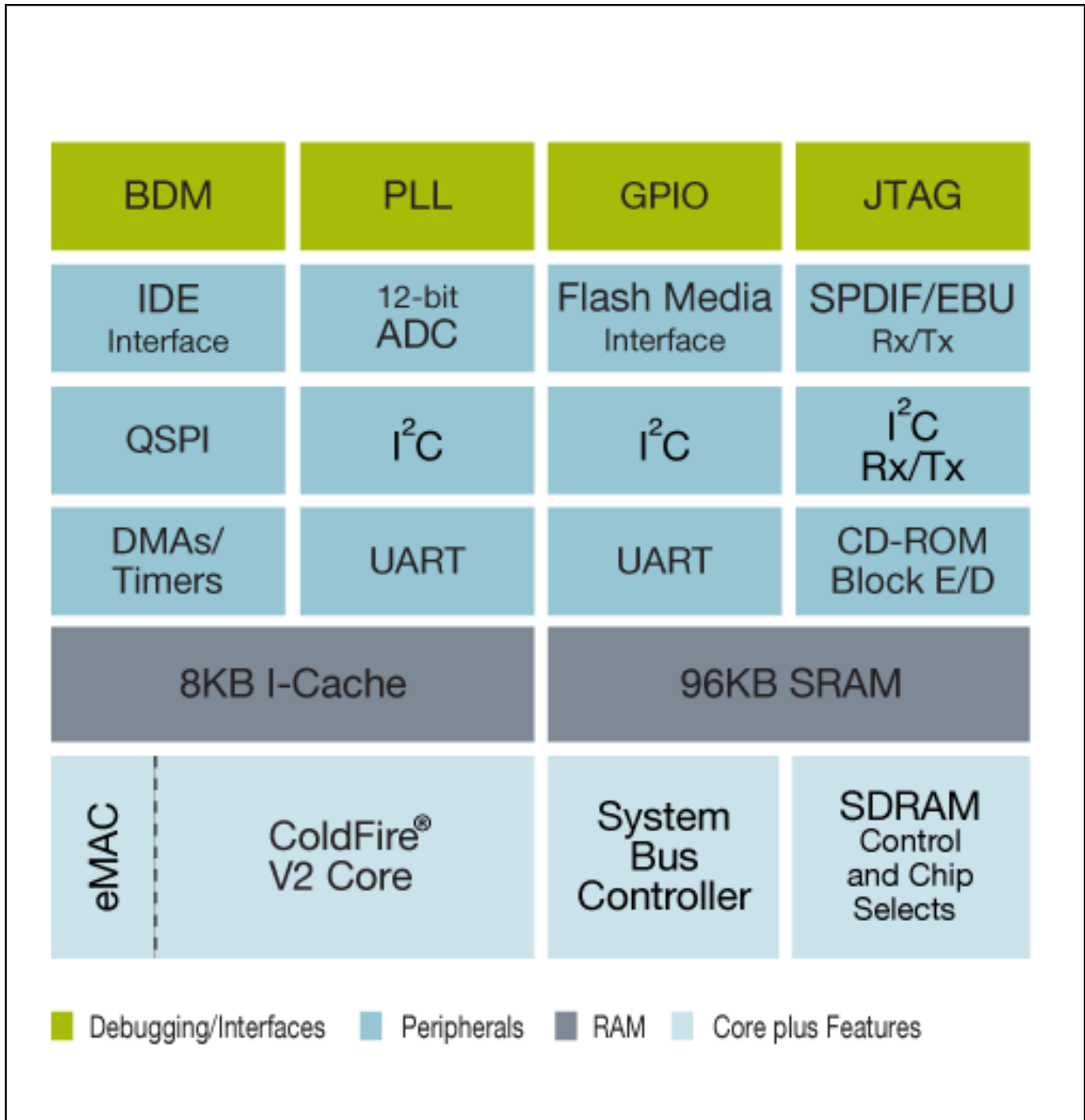
新規採用非推奨

このページでは、新規設計を推奨しない製品に関する情報を掲載しています。

Last Updated: Apr 9, 2022

The MCF5249 microprocessor delivers a high level of performance for the popular ColdFire® V2 core with over 125 (Dhrystone 2.1) MIPS @ 140 MHz and the lowest power consumption (1.3 mW/MHz) on a ColdFire device. This highly integrated microprocessor builds upon the ColdFire peripheral mix by adding audio enhancements and analog functionality in addition to an enhanced multiplier-accumulator unit (eMAC), large on-chip memory (96 KB SRAM, 8 KB I-Cache) and a SDRAM controller. The device is ideal for applications requiring significant control processing for file management, data buffering and user interface, as well as signal processing in key markets such as security, gaming, medical and digital audio. This leading package of integration, high performance and low power consumption allows fast time to market through access to high quality software modules, easy code reuse and extensive third-party tool support.

ColdFire MCF524X Microcontroller Block Diagram Block Diagram



View additional information for [Integrated ColdFire® Version 2 Microprocessor](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.