



System Basis Chip with CAN High-Speed and LIN Interface

MC34905

Not Recommended for New Designs

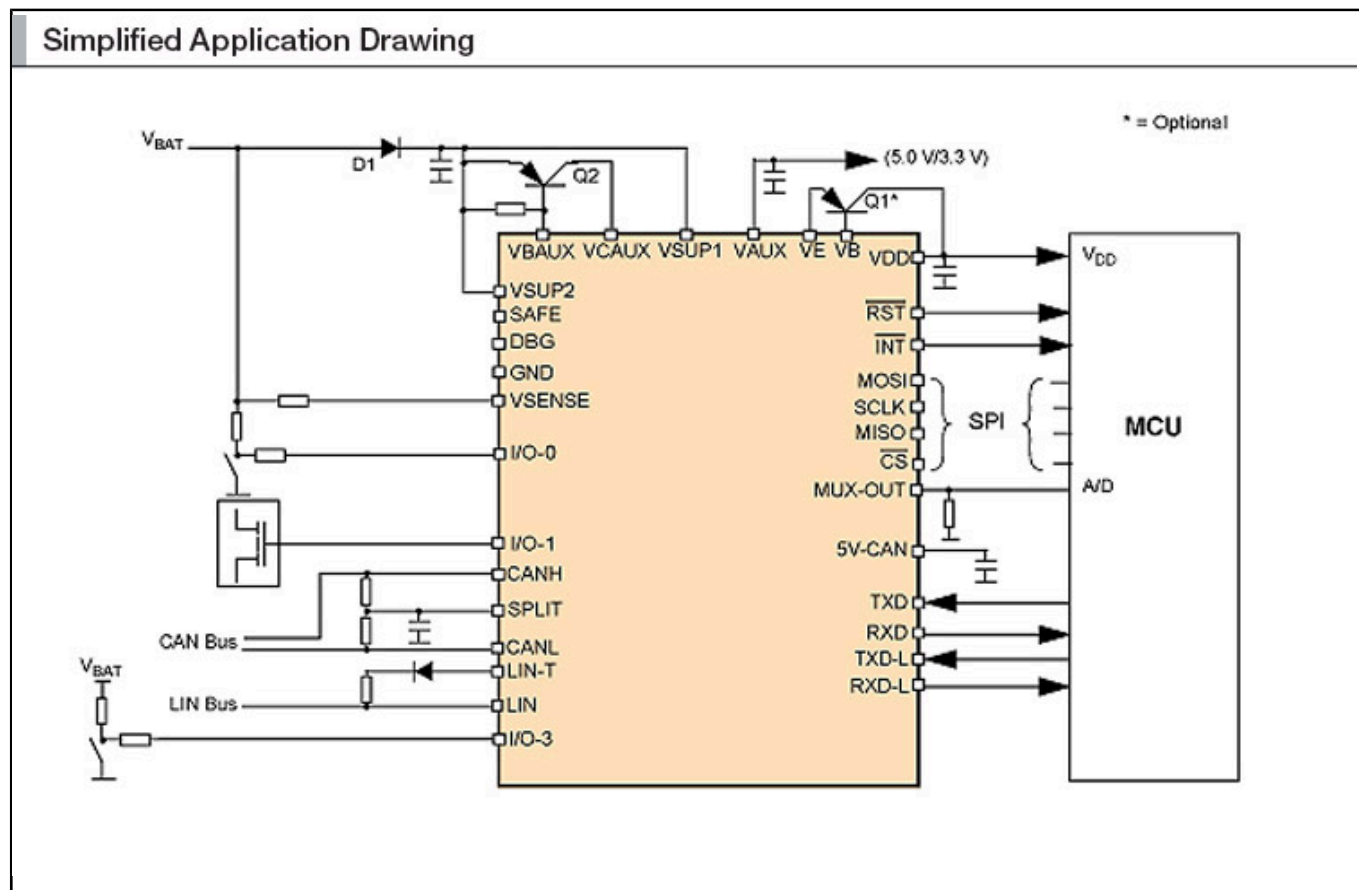
このページでは、新規設計を推奨しない製品に関する情報を掲載しています。

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The NXP® MC34905 system basis chip (SBC) with CAN high-speed and LIN interface. This chip is an analog companion chip for the MCU and combines voltage regulators, interface, monitoring and control, and other features to enhance module designs targeting the industrial market.

- Built-in enhanced high-speed CAN interface (ISO11898-2 and -5) with local and bus failure diagnostics, protection, and fail-safe operation modes
- The SBC may include two LIN 2.1 interfaces with LIN output pin switches
- Includes up to four wake-up input pins that can also be configured as output drivers for flexibility
- Powered by SMARTMOS® technology

Simplified Application Drawing Block Diagram



View additional information for [System Basis Chip with CAN High-Speed and LIN Interface](#).

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