



Arm7™ with 512 kB flash, 98 kB SRAM, Ethernet, USB 2.0 Device/Host/OTG, CAN, and 10-bit ADC

LPC2388FBD144

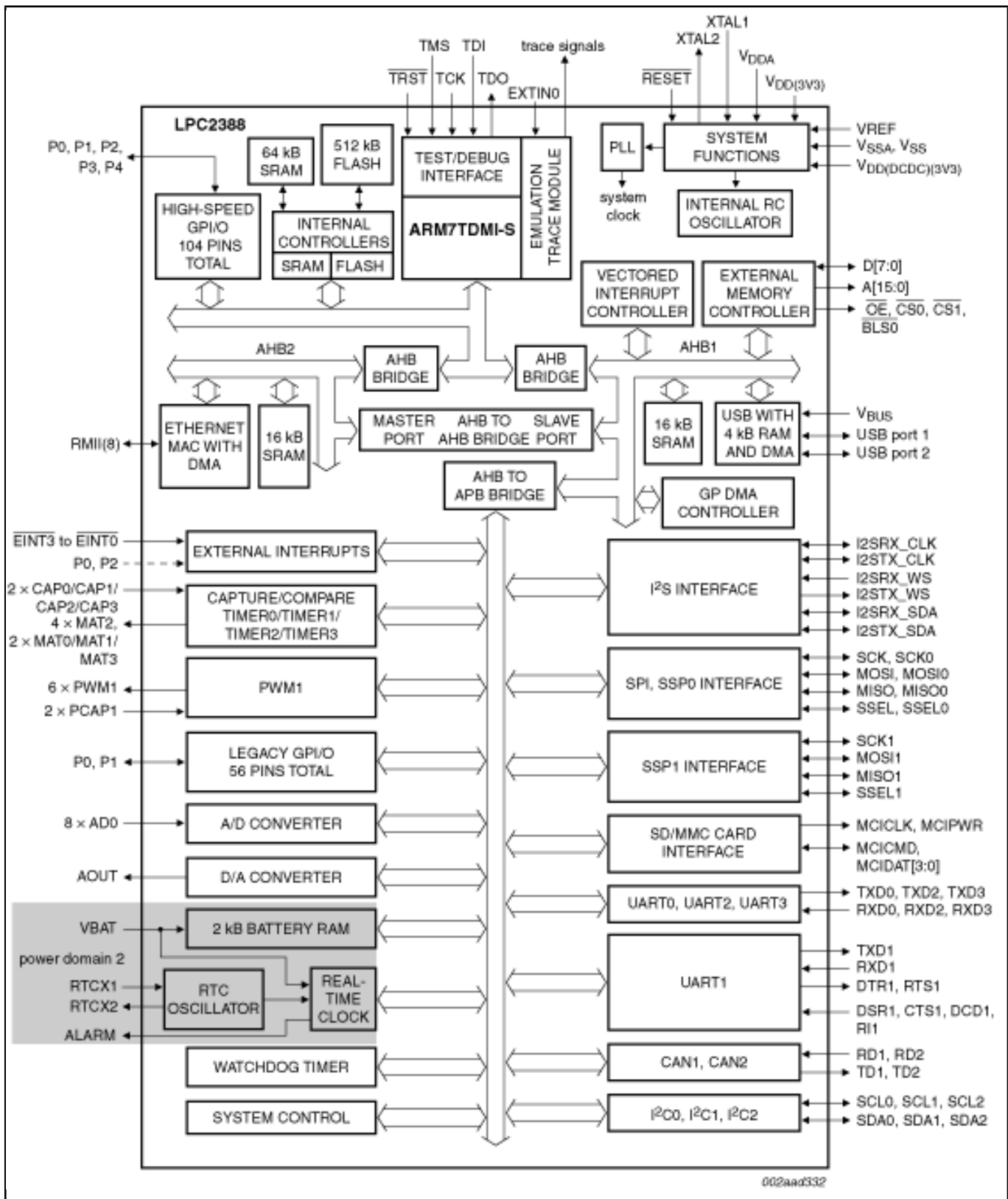
新規採用非推奨

このページでは、新規設計を推奨しない製品に関する情報を掲載しています。

Last Updated: Apr 8, 2022

The LPC2388 is an Arm7 microcontroller for embedded applications featuring a high level of integration and low power consumption at frequencies of 72 MHz. Features include 512 kB of flash memory, 98 kB of SRAM, Ethernet MAC, USB Device/Host/OTG, DMA controller, 4 UARTs, 2 CAN channels, 3 SSP/SPI, 3 I2C, I2S, 8-channel 10-bit ADC, 10-bit DAC, 2 PWM, 4 general purpose timers, low power Real-Time Clock with separate battery supply, and up to 160 general purpose I/O pins. The LPC2388 is pin-compatible to the LPC177x Cortex-M3 MCU series.

Block diagram: LPC2388FBD144 Block Diagram



View additional information for [Arm7™ with 512 kB flash, 98 kB SRAM, Ethernet, USB 2.0 Device/Host/OTG, CAN, and 10-bit ADC.](#)

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.