Kinetis® KL3x-48 MHz, Segment LCD Ultra-Low-Power Microcontrollers (MCUs) based on Arm® Cortex®-M0+ Core
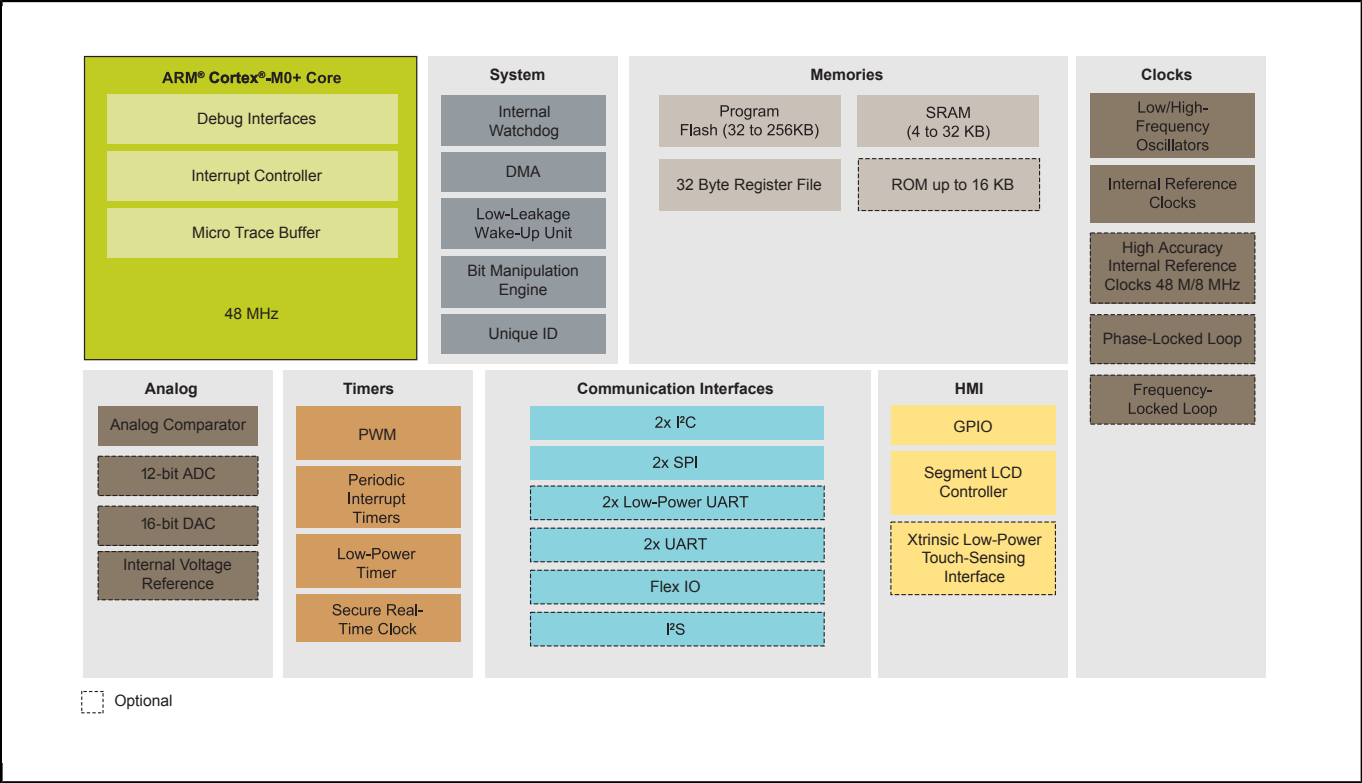
KL3x

Last Updated: Mar 5, 2025

The Kinetis® KL3x MCU family combines ultra-low power performance with a rich suite of analog, communication, timing, and control peripherals for consumer and industrial metering applications.

- Adds a segment LCD controller option to the Kinetis KL1x series
- Includes multiple, flexible low power modes including new compute mode that reduces dynamic power by placing peripherals in an asynchronous stop mode
- Provides a migration path to lower or higher performance and feature integration through compatibility with other Kinetis MCUs
- Backed by robust development tools and software

Kinetis L Series KL3x MCUs Block Diagram

ARM® Cortex®-M0+ Core
- Debug Interfaces
- Interrupt Controller
- Micro Trace Buffer

48 MHz

System
- Internal Watchdog
- DMA
- Low-Leakage Wake-Up Unit
- Bit Manipulation Engine
- Unique ID

Memories
- Program Flash (32 to 256KB)
- SRAM (4 to 32 KB)
- 32 Byte Register File
- ROM up to 16 KB

Clocks
- Low/High-Frequency Oscillators
- Internal Reference Clocks
- High Accuracy Internal Reference Clocks 48 M/8 MHz
- Phase-Locked Loop
- Frequency-Locked Loop

Analog
- Analog Comparator
- 12-bit ADC
- 16-bit DAC
- Internal Voltage Reference

Timers
- PWM
- Periodic Interrupt Timers
- Low-Power Timer
- Secure Real-Time Clock

Communication Interfaces
- 2x I²C
- 2x SPI
- 2x Low-Power UART
- 2x UART
- Flex IO
- I²S

HMI
- GPIO
- Segment LCD Controller
- Xtrinsic Low-Power Touch-Sensing Interface

Optional

View additional information for Kinetis® KL3x-48 MHz, Segment LCD Ultra-Low-Power Microcontrollers (MCUs) based on Arm® Cortex®-M0+ Core.

Note: The information on this document is subject to change without notice.