

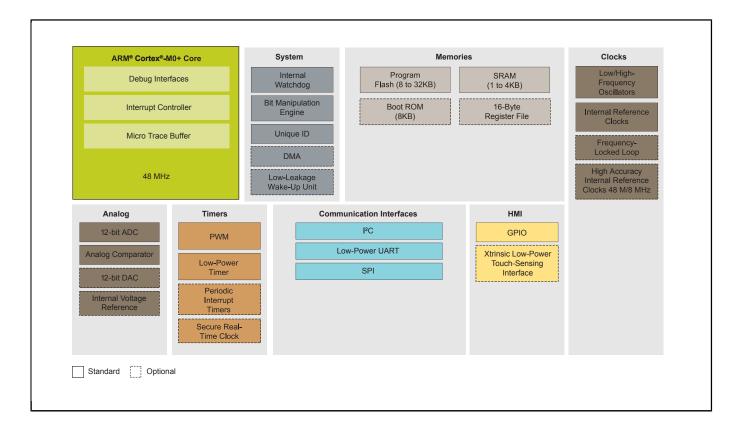
Kinetis® KL0x-48 MHz, Entry-Level Ultra-Low Power Microcontrollers (MCUs) based on Arm® Cortex®-M0+Core

KL₀x

Last Updated: Sep 5, 2025

The Kinetis® KL0x MCU family is the entry point into the Kinetis L series based on the Arm® Cortex®-M0+ core. The KL0x ultra-low-power MCUs provide a bridge for 8-bit customers migrating into the Kinetis portfolio and is software and tool compatible with all other Kinetis L series devices.

Kinetis L Series KL0x MCUs Block Diagram



View additional information for Kinetis® KL0x-48 MHz, Entry-Level Ultra-Low Power Microcontrollers (MCUs) based on Arm® Cortex®-M0+ Core.

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2025 NXP B.V.