



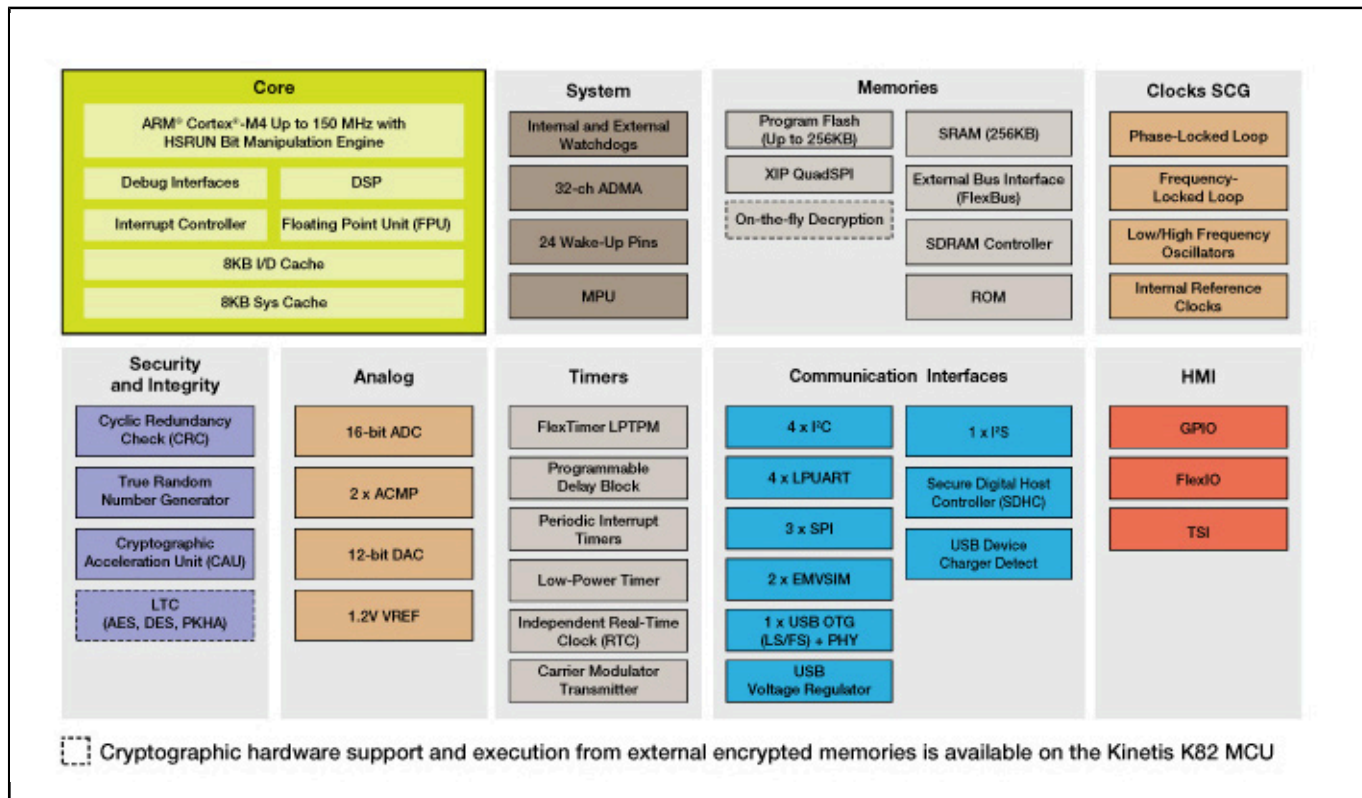
# **Kinetis® K80-150 MHz Advanced Security and QuadSPI Microcontrollers (MCUs) Based on Arm® Cortex®-M4 Core**

## **K80\_150**

Last Updated: Mar 5, 2025

The Kinetis® K80 MCU sub-family builds upon the Kinetis MCU portfolio with advanced security capabilities including Boot ROM to support encrypted firmware updates. The K80 MCU sub-family sub-family includes QuadSPI serial flash interface and separate voltage domain for a subset of IO pins. The QuadSPI interface supports connections to non-volatile memory for data or code. The extended memory resources allow developers to enhance their embedded applications with greater capability and features. These advancements are done while maintaining a high level of compatibility with the rest of the Kinetis MCU portfolio.

## Kinetis K80/K82 MCU Block Diagram Block Diagram



View additional information for [Kinetis® K80-150 MHz Advanced Security and QuadSPI Microcontrollers \(MCUs\) Based on Arm® Cortex®-M4 Core](#).

**Note:** The information on this document is subject to change without notice.

**www.nxp.com**

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2025 NXP B.V.