Kinetis® K65-180 MHz, Dual High-Speed and Full-speed USBs, 2MB Flash, Anti-Tamper Microcontrollers (MCUs) based on Arm® Cortex®-M4 Core

K65_180

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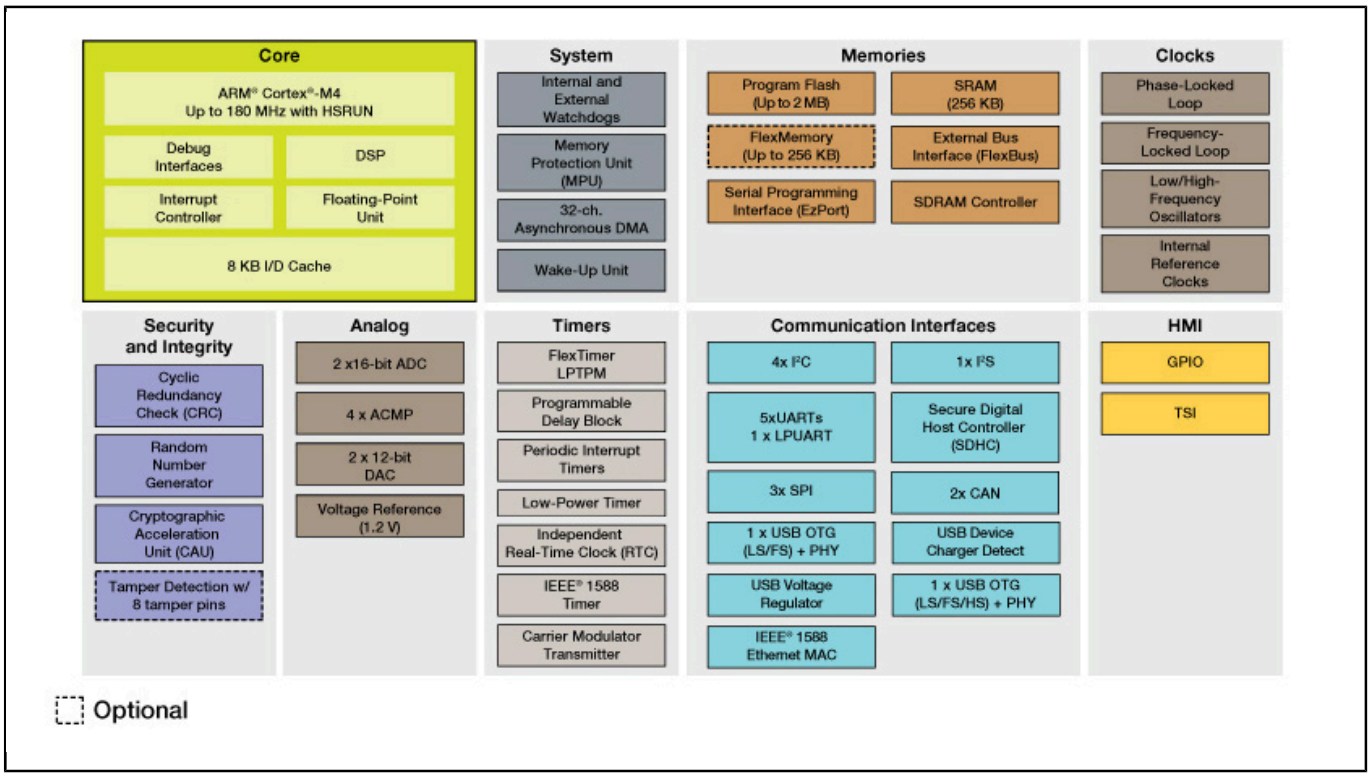
The Kinetis® K65 180MHz IEEE® 1588 Ethernet MCUs are built on the Arm® Cortex® -M4F core and are optimized for applications requiring security encryption, tamper detection, large memory densities and low-power processing efficiency. This family offers a High-Speed USB with integrated HS USB Physical Transceiver, SDRAM Controller, Full Speed Crystal-less USB support, as well as shares the comprehensive enablement and scalability of the Kinetis portfolio.

Please contact your local NXP representative to download the K65 Security Data sheet and Reference Manual documents (under NDA).

Kinetis K65/K66 MCU Family Block Diagram Block Diagram



View additional information for Kinetis® K65-180 MHz, Dual High-Speed and Full-speed USBs, 2MB Flash, Anti-Tamper Microcontrollers (MCUs) based on Arm® Cortex®-M4 Core.

Note: The information on this document is subject to change without notice.