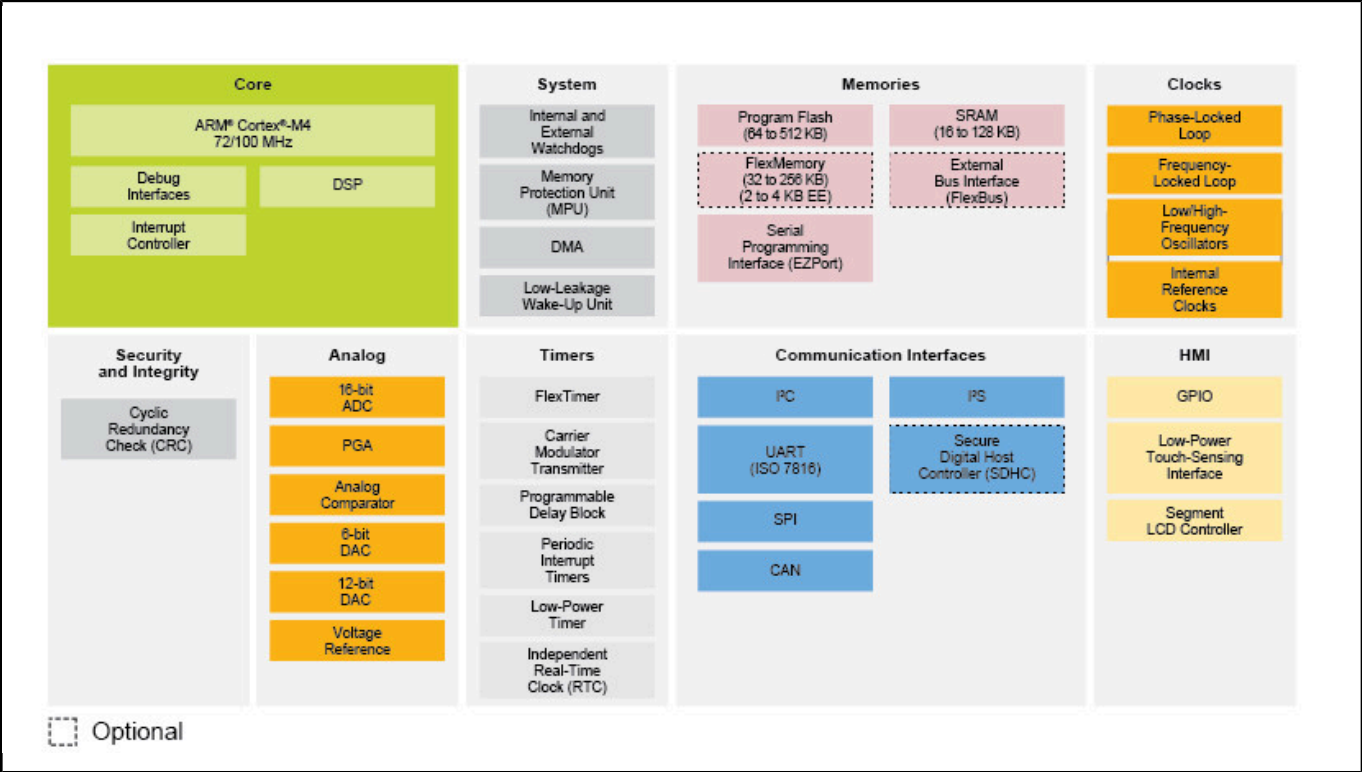# Kinetis® K30-100 MHz, Mixed-Signal Integration Microcontrollers (MCUs) based on Arm® Cortex®-M4 Core

K30_100

Last Updated: Jul 31, 2023

The Kinetis® K30 100 MHz Segment LCD MCUs are built on the Arm® Cortex®-M4 core and feature advanced analog integration and serial communication. This family shares the comprehensive enablement and scalability of the Kinetis portfolio.

Kinetis K30 MCU Family Block Diagram Block Diagram

Core	System	Memories	Clocks
ARM® Cortex®-M4 72/100 MHz	Internal and External Watchdogs	Program Flash (64 to 512 KB) / SRAM (16 to 128 KB)	Phase-Locked Loop
Debug Interfaces / DSP	Memory Protection Unit (MPU)	FlexMemory (32 to 256 KB) (2 to 4 KB EE) / External Bus Interface (FlexBus)	Frequency-Locked Loop
Interrupt Controller	DMA	Serial Programming Interface (EZPort)	Low/High-Frequency Oscillators
	Low-Leakage Wake-Up Unit		Internal Reference Clocks

Security and Integrity	Analog	Timers	Communication Interfaces	HMI
Cyclic Redundancy Check (CRC)	16-bit ADC	FlexTimer	I²C / I²S	GPIO
	PGA	Carrier Modulator Transmitter	UART (ISO 7816) / Secure Digital Host Controller (SDHC)	Low-Power Touch-Sensing Interface
	Analog Comparator	Programmable Delay Block	SPI	Segment LCD Controller
	6-bit DAC	Periodic Interrupt Timers	CAN	
	12-bit DAC	Low-Power Timer		
	Voltage Reference	Independent Real-Time Clock (RTC)		

☐ Optional

View additional information for Kinetis® K30-100 MHz, Mixed-Signal Integration Microcontrollers (MCUs) based on Arm® Cortex®-M4 Core.

Note: The information on this document is subject to change without notice.