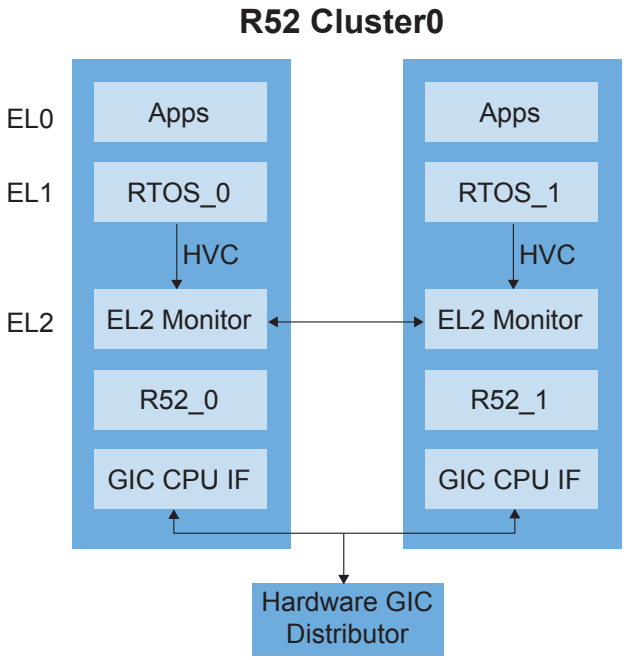
EL2 Monitor Partitioning Hypervisor for Arm® Cortex®-R52 (EL2M)

EL2M

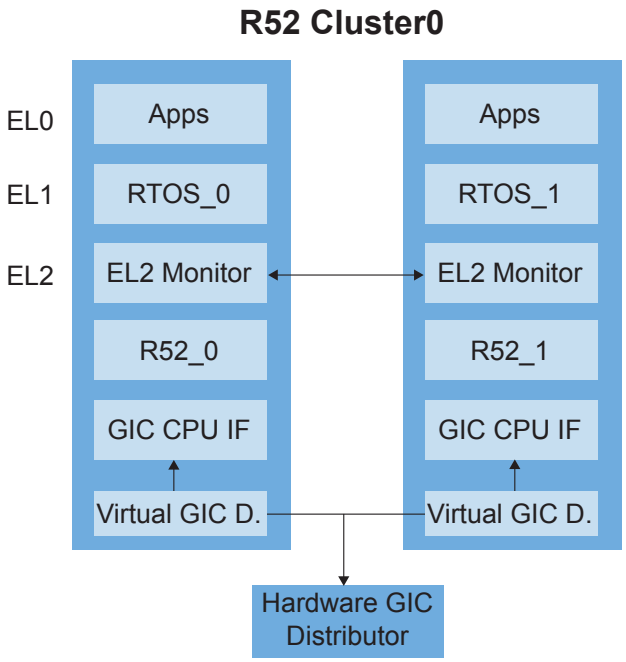
Last Updated: Apr 30, 2026

Exception Level 2 (EL2) Monitor is NXP's lightweight partitioning hypervisor built for Arm® Cortex®-R52 cores and certified to ASIL D—the highest automotive safety level. It delivers robust isolation so each software component runs securely and independently. With its minimal footprint and tight integration with NXP hardware, EL2 Monitor delivers unmatched safety, efficiency and reliability.

EL2 Monitor Paravirtualization Diagram Block Diagram



EL2 Monitor Trap and Emulation Diagram Block Diagram



View additional information for EL2 Monitor Partitioning Hypervisor for Arm® Cortex®-R52 (EL2M).

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