



# Digital Signal Controller

## DSP56F827

新規採用非推奨

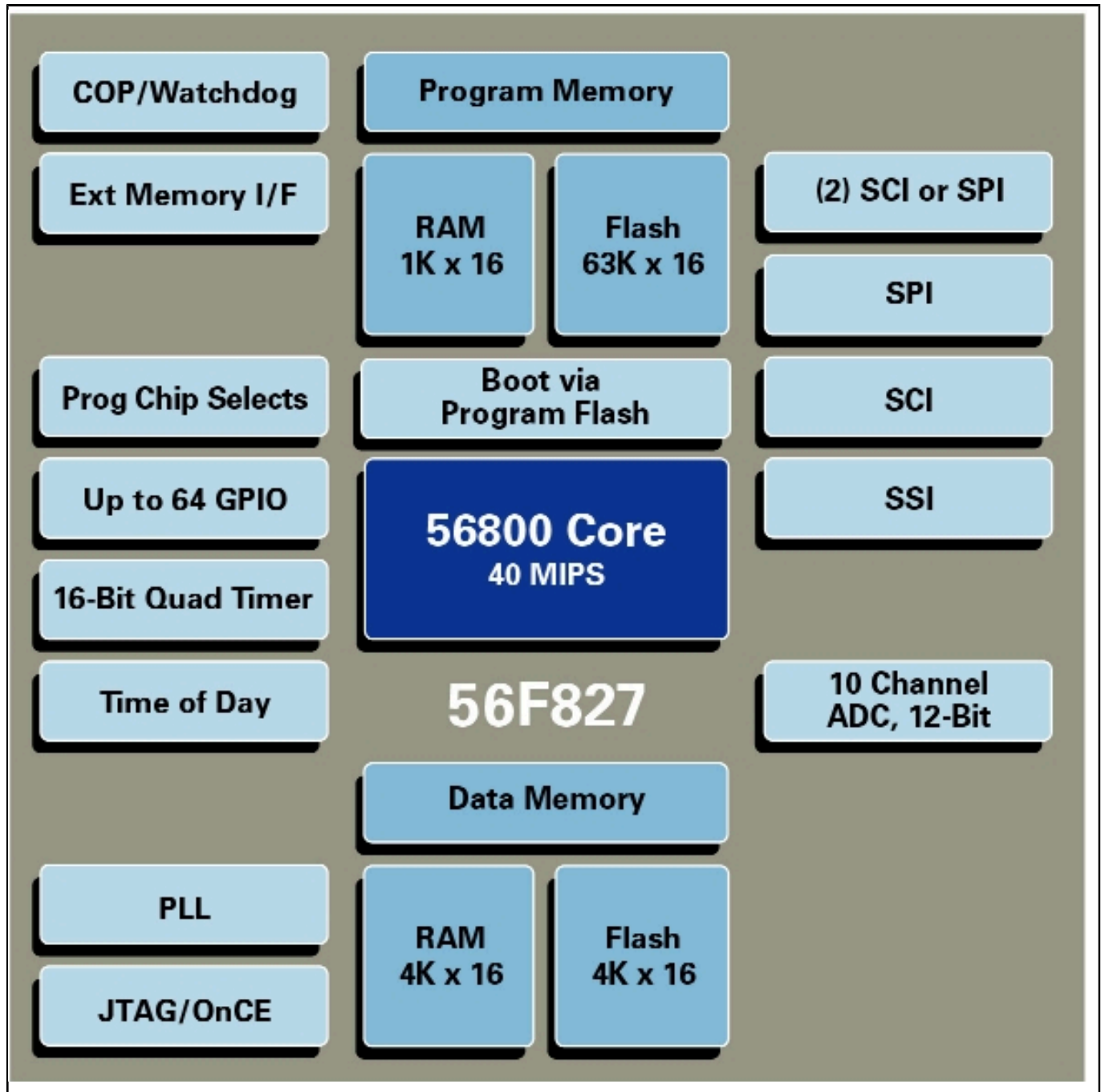
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Last Updated: Apr 9, 2022

The 56F827, a member of the 56800 core-based family of Digital Signal Controllers, combines the processing power of a DSP and the functionality of a microcontroller with a flexible set of peripherals on a single chip. The 56F827 is ideal for a multitude of control, telephony and other applications requiring highly-integrated, high-performance solutions with all the flexibility enabled by Flash technology.

The 56800 core is based on a Harvard-style architecture consisting of three execution units which operate in parallel, allowing as many as six operations per instruction cycle. The microprocessor-style programming model and optimized instruction set allow straightforward generation of efficient, compact code for both DSP- and MCU-style applications. The instruction set is also highly efficient for C compilers, enabling rapid development of optimized control applications.

# DSP56F827\_BLKDIAG Block Diagram



View additional information for [Digital Signal Controller](#).

Note: The information on this document is subject to change without notice.

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