Digital Signal Controller

DSP56F826

Not Recommended for New Designs

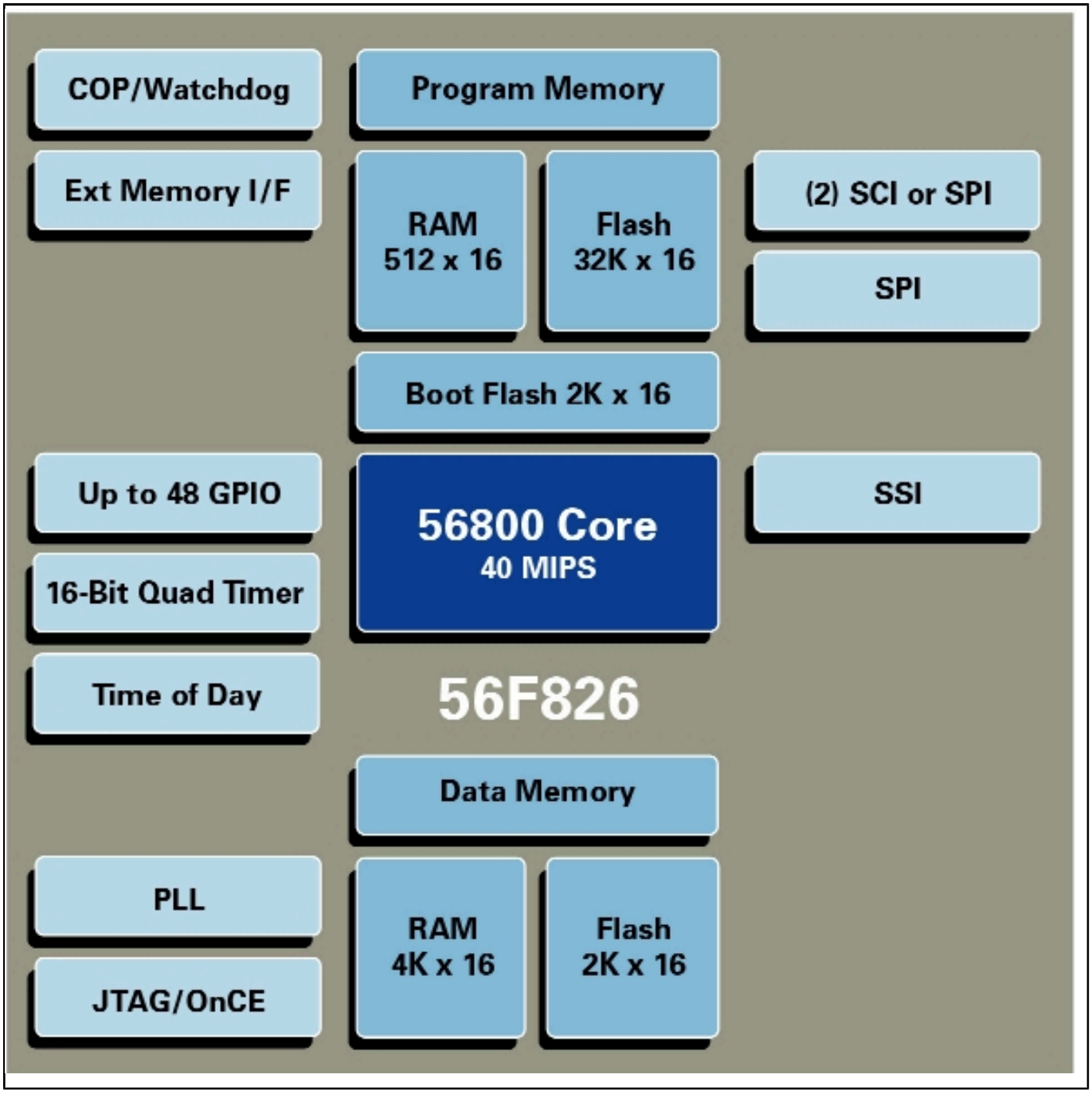
このページでは、新規設計を推奨しない製品に関する情報を掲載しています。

Last Updated: Apr 9, 2022

The 56F826, a member of the 56800 core-based family of Digital Signal Controllers, combines the processing power of a DSP and the functionality of a microcontroller with a flexible set of peripherals on a single chip. The 56F826 is ideal for a multitude of control, telephony and other applications requiring highly-integrated, high-performance solutions with all the flexibility enabled by Flash technology.

The 56800 core is based on a Harvard-style architecture consisting of three execution units which operate in parallel, allowing as many as six operations per instruction cycle. The microprocessor-style programming model and optimized instruction set allow straightforward generation of efficient, compact code for both DSP- and MCU-style applications. The instruction set is also highly-efficient for C Compilers, enabling rapid development of optimized control applications.

DSP56F826 Block Diagram Block Diagram



View additional information for Digital Signal Controller.

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.