Ara240 Discrete Neural Processing Unit (DNPU)

ARA240

Preproduction

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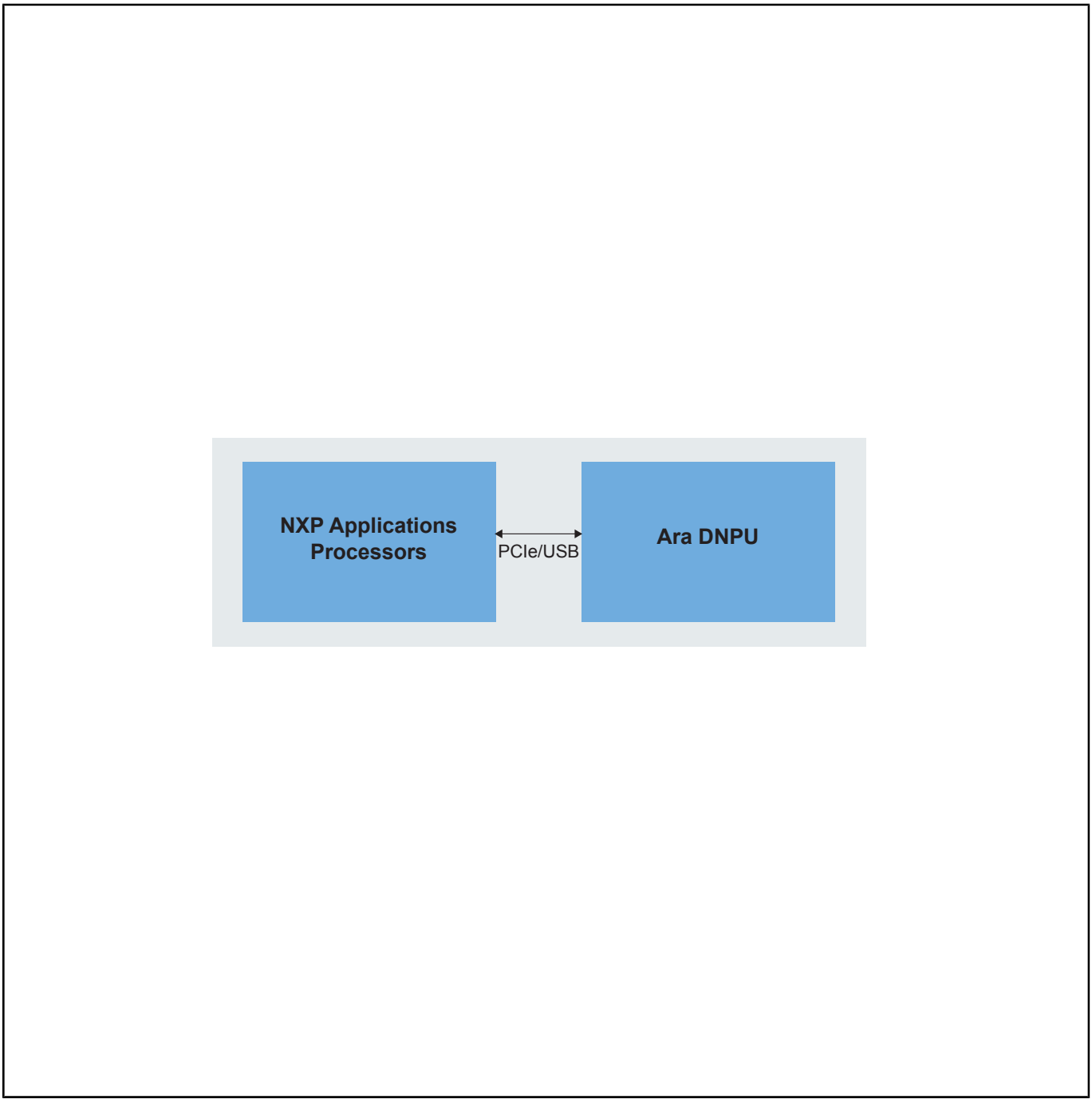
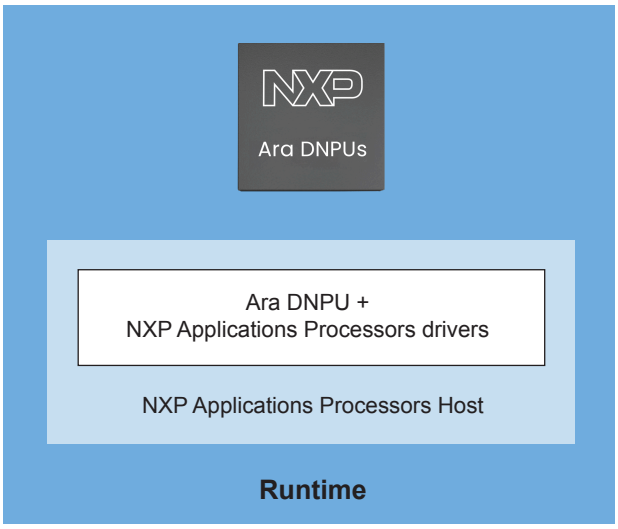
The Ara240 Discrete Neural Processing Unit (DNPU) enables real-time generative AI, large language models (LLMs) and vision language models (VLMs) execution on AI-enabled compute and embedded systems, delivering low-latency, lower operational costs and enhanced data privacy. Its innovative architecture combines balanced compute, large on-chip memory and high off-chip bandwidth to efficiently execute large models.

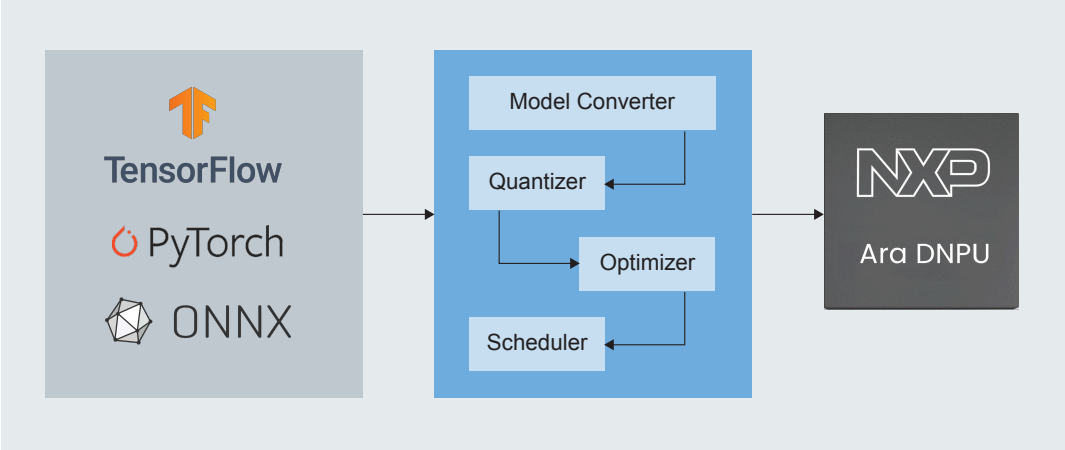
NXP Applications Processor and Ara DNPU Connection Block Diagram



Integration of Ara DNPU and Applications Processors Host with Drivers Block Diagram



Machine learning deployment flow Block Diagram



View additional information for Ara240 Discrete Neural Processing Unit (DNPU).

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